

REMARKS

This is a full and timely response to the outstanding non-final office action electronically delivered on November 3, 2008. Applicants hereby respectfully request entry of the amendments to Claims 1-7 and paragraph [0037] of the specification as set forth hereinbefore to place the present application in condition for allowance. No new matter has been added to the application by virtue of the present amendments, and reconsideration and allowance of the application and presently pending Claims 1 and 3-7, as amended, are earnestly requested.

Present Status of the Application

Claims 1-7 are rejected under 35 U.S.C. 112 as failing to comply with the written description requirement.

In response thereto, Applicants removes all the description about “storage capacities” and “the number of pins” in Claims 1-7, so as to comply with the written description requirement. Moreover, Applicants submits that “the multi-memory architecture has a pin configuration same as that of the first type memory” as set forth in the amended Claim 1 can be supported by FIG. 1 and the description of “the overall pin configuration of this multi-memory architecture is arranged to be the same as the pin configuration of the generic 64MB Mask ROM for compatibility” as disclosed in the paragraph [0037] of the specification. No new matter has been added to the application by virtue of the present amendments, and reconsideration and allowance of the application and presently pending Claims 1 and 3-7, as amended, are earnestly requested.

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Application No.: 10/064,916

Drawings are objected to under 37 CFR 1.83(a) as failing to show every feature of the invention specified in the claims.

In response thereto, Applicants submits that the features of "...a second type non-volatile memory device having a second data storage capacity and a second predefined pin configuration having a second number of pins which is an actual number of pins according to the second data storage capacity; wherein the first number of pins is greater than the second number of pins, the total pin number of the multi-memory architecture is equal to the number of pins of the first type non-volatile memory device of the multi-memory architecture and the first type non-volatile memory device and the second type non-volatile memory device are disposed in the multi-memory architecture at the same time" are canceled from the Claim 1, such that the drawings have shown every feature of the invention specified in the claims, and reconsideration and allowance of the application and drawings are earnestly requested.

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CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 1 and 3-7 of the present application patently define over the prior art and are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Respectfully submitted,

Date :

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